Assignee: Intel Corporation

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of claims:

Claims 1-12. (Canceled).

- 13. (Currently amended) An apparatus to decrease resonance in a printed circuit board, comprising:
 - a signal trace line for carrying to carry a signal;
- a ground plane for connecting to connect said signal trace line to a ground;
- a cut in said ground plane for increasing to increase the transit time of said signal through said ground plane to decrease resonance in a printed circuit board.
- 14. (Currently amended) An apparatus in accordance with claim 13, further to decrease resonance in a printed circuit board, comprising:
 - a signal trace line to carry a signal;
 - a ground plane to connect said signal trace line to a ground;
- <u>a cut in said ground plane to increase the transit time of said signal</u> through said ground plane; and
- an additional length segment within said signal trace line for increasing to increase the transit time of said signal through said signal trace line;
- said additional length segment when added to said signal trace line increases the transit time at said signal through said signal trace line out of a resonance range.

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- 15. (Original) An apparatus in accordance with claim 13, wherein: said cut is oriented substantially perpendicular to a long axis of the PCB.
- 16. (Currently amended) A claim An apparatus in accordance with claim 13, wherein:

said cut is continuous.

- 17. (Currently amended) A claim An apparatus in accordance with claim 13 wherein: said cut is a zipper cut non-continuous.
- 18. (Currently amended) A claim in accordance with claim 13, An apparatus to decrease resonance in a printed circuit board, comprising:
 - a signal trace line to carry a signal;
 - a ground plane to connect said signal trace line to a ground; and
- <u>a cut in said ground plane to increase the transit time of said signal through said ground plane;</u>

wherein:

said cut terminates more than 10 mils from said signal trace line.

- 19. (Currently amended) An apparatus in accordance with claim 13, to decrease resonance in a printed circuit board, comprising:
 - a signal trace line to carry a signal;
 - a ground plane to connect said signal trace line to a ground; and
- <u>a cut in said ground plane to increase the transit time of said signal</u> through said ground plane;

wherein: a plurality of said cuts are similarly located with a PCB in respective ground plane layers of said printed circuit board.